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May 17, 2005

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Certificate
JUN 2 0 2005
of Correction

Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. 6,889,284
Issued: May 3, 2005
For: METHOD AND APPARATUS FOR
SUPPORTING SDRAM MEMORY
Inventor: Nizar et al.
<u>Our File No. 42390.P7149</u>

Dear Sir:

Enclosed is the Certificate of Correction (two copies) for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Edwin H. Taylor
Reg. No. 25,129

EHT/kh
Enclosures

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.   : 6,889,284 B1

DATED   : May 3, 2005

INVENTOR(S)   : Nizar et al.

    It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 4, at line 67, delete "1" and insert --=1--.

In column 6, at the end of line 18 after "row.", insert --MA[7.0]=Byte Masks for DQA bus.--.

In column 8, at line 42 after "generated", insert --by--.

In column 10, at line 43, delete "representative" and insert --responsive--.

UNITED STATES PATENT AND TRADEMARK OFFICE
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                    Certificate of Correction (PTO Form 1050)-Amended